

L Number	Hits	Search Text	DB	Time stamp
1	686	(separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1) ("001.ab.").PN.	USPAT	2004/08/20 10:20
2	0		USPAT	2004/08/20 10:20
3	0	("01.clm.").PN.	USPAT	2004/08/20 10:20
4	38	1.ab.	USPAT	2004/08/20 10:21
5	87	1.clm.	USPAT	2004/08/20 10:31
6	141	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) and 710/\$.ccls.	USPAT	2004/08/20 10:33
7	164	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) and 711/\$.ccls.	USPAT	2004/08/20 10:32
8	0	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) same (plurality near2 (directors boards processors))	USPAT	2004/08/20 10:33
9	89	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) and (plurality near2 (directors boards processors))	USPAT	2004/08/20 10:33
10	22	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) and (plurality near2 (directors boards processors))) and 710/\$.ccls.	USPAT	2004/08/20 10:34
11	132	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) same (directors boards processors)	USPAT	2004/08/20 10:33
12	23	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) same (directors boards processors)) and 710/\$.ccls.	USPAT	2004/08/20 10:34
13	33	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) and (plurality near2 (directors boards processors))) and 711/\$.ccls.	USPAT	2004/08/20 10:34
14	45	((separat\$3 differen\$3) near4 ((data near2 transfer) (cache adj memory)) near4 (message\$1 instruction\$1 request\$1 signal\$1)) same (directors boards processors)) and 711/\$.ccls.	USPAT	2004/08/20 10:34
-	1	5903911.pn.	USPAT	2004/08/19 10:31
-	1	6651130.pn.	USPAT	2004/08/19 14:49

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-	19	US-4780808-\$.DID. OR US-5274645-\$.DID. OR US-0538651-\$.DID. OR US-5551048-\$.DID. OR US-5586264-\$.DID. OR US-5603058-\$.DID. OR US-5745790-\$.DID. OR US-5787265-\$.DID. OR US-0590391-\$.DID. OR US-5920893-\$.DID. OR US-5948062-\$.DID. OR US-5949982-\$.DID. OR US-6055603-\$.DID. OR US-6205536-\$.DID. OR US-6240335-\$.DID. OR US-6338095-\$.DID. OR US-6304903-\$.DID. OR US-6418496-\$.DID. OR US-6438586-\$.DID.	USPAT	2004/08/19 14:49
-	1	4977556.PN.	USPAT	2004/08/19 14:50
-	1	4476526.PN.	USPAT	2004/08/19 14:50
-	1	4633387.PN.	USPAT	2004/08/19 14:50
-	1	6038641.PN.	USPAT	2004/08/19 14:53
-	0	US-E0510821-\$.DID. OR US-E0795812-\$.DID. OR US-J8172446-\$.DID. OR US-J07244634-\$.DID. OR US-J07244634-\$.DID.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 14:54
-	0	E0510821-\$.DID. OR E0795812-\$.DID. OR J8172446-\$.DID. OR J07244634-\$.DID. OR J07244634-\$.DID.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/20 10:14

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